OPERATIONAL AMPLIFIERS (OP-AMPS) II

LAB 5 INTRO: INTRODUCTION TO INVERTING AMPLIFIERS AND OTHER OP-AMP CIRCUITS

GOALS

In this lab, you will characterize the gain and frequency dependence of inverting op-amp circuits. You will work with a few applications of negative feedback including a circuit to sum voltages.

Proficiency with new equipment:

- Inverting op-amps: basic, summing, differentiator, and integrator

Experimental Design:

- Designing, building, and characterizing your own op-amp circuit

Modeling the physical system:

- Frequency dependence of op-amp circuits
- Input and output impedances of op-amp circuits

Applications:

- Build a digital to analog converter

DEFINITIONS

Closed-loop gain, G – gain of the *op-amp circuit* at all frequencies with feedback applied
Low frequency gain, G_0 – gain of the *op-amp circuit* at DC (f = 0 Hz)
Open-loop gain, A – gain of the *op-amp itself* at all frequencies with no feedback applied
DC gain, A_0 – gain of the *op-amp itself* at DC (f = 0 Hz) with no feedback applied
f_0 – 3 dB frequency for an *op-amp itself* with no feedback
f_B – 3 dB frequency for an *op-amp circuit* with feedback applied
f_T – unity gain frequency, frequency where the open loop gain A is equal to one

INVERTING AMPLIFIERS

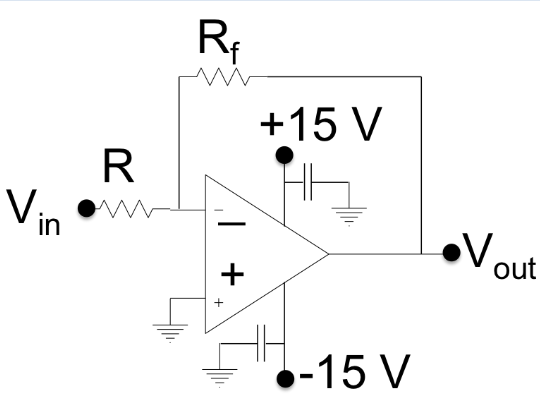

Figure 1. Inverting Amplifier

The basic inverting amplifier is shown in Figure 1. We can use the Golden Rules to determine the low-frequency gain. Since the positive input is grounded, the op-amp will do everything it can to keep the negative input at ground as well. In the limit of infinite open loop gain the inverting input of the op-amp is a virtual ground, a circuit node that will stay at ground as long as the circuit is working, even though it is not directly connected to ground. When ***A is infinite,*** the gain of an inverting amplifier is

$$G_0 = \frac{V_{out}}{V_{in}} = -\frac{R_f}{R}$$

This is the "Golden Rule" result. For the frequency dependence of the gain we need to consider what happens when A is not infinite, as we did for the non-inverting case. We use the same definitions as for the non-inverting case. The op-amp open loop gain is A, and the divider ratio B is the same as before:

$$B = \frac{R}{R + R_f}$$

When ***A is finite*** the gain for an inverting amplifier is given by

$$G_0 = \frac{V_{out}}{V_{in}} = -\frac{A_0(1-B)}{1 + A_0 B}$$

The above formulas are still correct when A and/or B depend on frequency. B will be frequency independent if we only have resistors (in other cases that use complex impedance it may not be), but A always varies with frequency. For most op-amps, including the LF356, the open loop gain varies with frequency like an RC low-pass filter:

$$A = \frac{A_0}{1 + j\frac{f}{f_0}}$$

The 3dB frequency, f_0, is usually very low, around 10 Hz. Data sheets do not usually give f_0 directly; instead they give the dc gain, A_0, and the unity gain frequency f_T, which is the frequency where the magnitude of the open loop gain A is equal to one. The relation between A_0, f_0, and f_T is

$$f_T = A_0 f_0$$

The frequency dependence of the closed loop gain G is then given by

$$G = \frac{G_0}{1 + j\frac{f}{f_B}}$$

The frequency response of the amplifier with feedback is therefore also the same as for an RC low-pass filter.

Input and Output Impedances of Inverting Amplifiers

Formulas for the input and output impedance for an inverting amplifier are derived in H&H Section 4.26. When the open loop gain is large, the negative input of the op-amp is a virtual ground and so the input impedance is just equal to R. This is very different from the non-inverting case where the input impedance is proportional to A for large A. In practice, the input impedance of an inverting amplifier is not usually greater than about 100 kΩ, while the input impedance of a non-inverting amplifier can easily be as large as 10^{12} Ω. When A is not large the formulas for the input impedance and output impedance of the entire circuit are derived in H&H Section 4.26. The results are

$$R_i' = R + \frac{R_f}{1 + A}$$

$$R_o' = R_o/(1 + AB)$$

The output impedance is the same for both the inverting and non-inverting amplifiers.

The gain-bandwidth product for inverting amplifier is slightly modified compared to non-inverting amps.

$$A_0 f_0 = \frac{-G_0 f_B}{1-B} = f_T$$

This is the same (except for the sign) as the non-inverting result when the closed loop gain is large (B << 1, $|G_0|$ >> 1), but at unity closed loop gain (B = 1/2, G_0 = –1) the inverting amplifier has only half as much bandwidth as a non-inverting amplifier.

SUMMING AMPLIFIERS

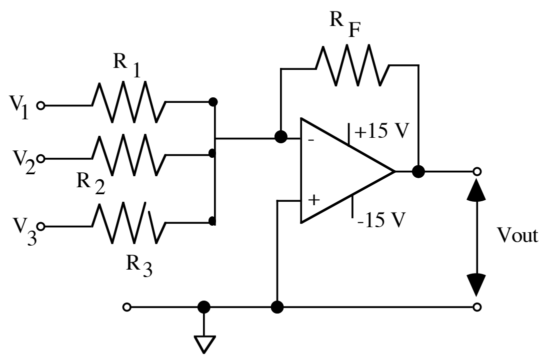

Figure 2. Summing Amplifier

The Summing Amplifier, shown in Fig. 2, is a very flexible circuit based upon the standard inverting op-amp configuration that can be used for combining multiple inputs. The standard inverting amplifier has a single input voltage, Vin, applied to the inverting input terminal. If we add more input resistors to the input, the circuit can become a voltage adder with different gain for each input. There are many applications for summing amplifiers including audio mixers and digital to analog converters. Using the Golden Rules we can determine the transfer function listed below.

$$V_{out} = -\left[V_1\left(\frac{R_F}{R_1}\right) + V_2\left(\frac{R_F}{R_2}\right) + V_3\left(\frac{R_F}{R_3}\right)\right]$$

This circuit has many applications including working as an adder in any basis-set you specify. In class we gave an example of 0-9 in fixed integers (decimal). If you only restrict yourself to input voltages of 0 or 1 V (or on and off), you get to a binary adder and can convert binary signals to analog (e.g. base 2 to base 10). Digital-to-analog converters are found in every research lab (or computer, or phone, etc.) where you want to create any value of a signal from just 1's and 0's. If you want a refresher on binary or counting in binary, you might enjoy visiting the Wikipedia: http://en.wikipedia.org/wiki/Binary_number - Counting_in_binary

INTEGRATOR

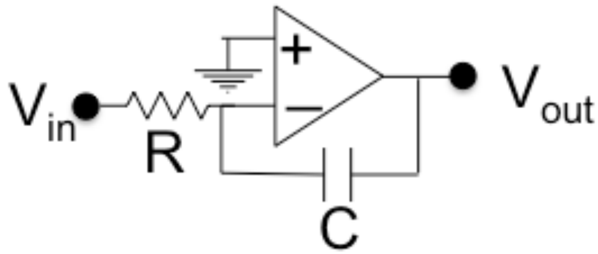

Figure 3. Integrator

The basic op-amp integrator is shown in Fig. 3. As compared to the inverting amplifier in Fig. 1, the only change is a replacement of the feedback resistor R_f with a capacitor C. An integrator is the same circuit as a low-pass filter. If you are interested in what frequencies get passed, you call it a low-pass filter. If you are interested in integrating the input signal, you call it an integrator. We can understand how the integrator works by applying the Golden Rules and remembering the defining equation for capacitors. Just as in the integrating amplifier, the Golden Rules tells us that $V_{-} \approx V_{+} = 0$ and that the current across both components is the same because no current flows into the op-amp. We label I_R as the current through the resistor, which is V_{in}/R since V_{-} is at ground. We label I_C as the current across the capacitor. The defining equation for capacitors is Q=CV. The current through a capacitor is I = dQ/dt = d(CV)/dt = CdV/dt. Therefore, the equation $I_R = I_C$ becomes:

$$\frac{V_{in}}{R} = -C\frac{dV_{out}}{dt}$$

Solving this equation for V_{out} gives the following:

$$V_{out} = -\frac{1}{RC}\int V_{in}\,dt$$

This is why it is called an integrator. Choosing values for the resistor and capacitor depends on several considerations. First, one needs to determine the value of the product RC. This will depend on the expected signal (size and shape), the expected integration time, and the desired output (remembering the op-amp limitations on maximum output voltage and slew rate). Once RC is determined, some considerations for choosing R and C are:

- Availability: resistors from 1 Ω to 10 MΩ and capacitors from 2 pF to 1 μF are readily available.
- Input impedance: generally a high input impedance (> 1 kΩ) is desired to avoid loading the input signal.
- Avoiding effect of stray capacitance: there are many sources of capacitance, such as cables, which can unintentionally couple to your circuit. For this reason, it is advisable to avoid very small capacitors as the stray capacitances may end up dominating. A few hundred pF should be sufficient.
- Dealing with DC signal: A DC signal in the input results in the capacitor eventually charging up to the maximum output voltage of the op-amp. This DC signal can result from the op-amp itself. One way to avoid this is to add another resistor in parallel with the capacitor such that at low frequencies (large impedance in capacitor) the circuit acts like an inverting amplifier. As the impedance of a resistor is proportional to R while the impedance of a capacitor is inversely proportional to C, to ensure that the signal mainly goes through the capacitor, one would like large resistor and/or large capacitor values. Of course, the resistor chosen here in combination to the main resistor in the circuit leads to a DC gain as calculated for the inverting amplifier so this should be considered in making your choice.

USEFUL READINGS

1. FC Sections 12.2–12.15. The basic rules of op-amp behavior and the most important op-amp circuits. [Note while FC discussed basic characteristics of op amps in 12.2 it does not have the “Golden Rule” analysis that we will discuss in class.] Do not worry right now about the transistor guts of op-amps. We will learn about transistors in Experiments 7-8.
2. Horowitz and Hill, Sections 4.04-4.08, 4.19-4.20 and Sections 1.13-1.15

LAB PREP ACTIVITIES

Answer the following questions using Mathematica for the plots. You can use either Mathematica for the rest the questions as well or do them by hand in your lab book. **Bring an electronic copy of your notebook to lab, preferably on your own laptop. You will use it to plot your data during the lab session.**

Question 1	**Inverting amplifier** a. Calculate the values of low frequency gain G_0 and the bandwidth f_B for the inverting amplifier in Fig 1 for the following circuit you will build in lab with the following resistors. 1) R_F = 100 kΩ, R=10 kΩ b. Graph a Bode plot for the open loop gain and the closed loop gain for the circuit from part (a) on the same graph using Mathematica.
Question 2	**Summing Amplifier – Digital to Analog Converter** a. Design a three input summing amplifier (Fig. 2) that can create an analog voltage of integers from 0 to -7 volts ($\|V_{out}\|$= 0,1,2,3,4,5,6, and 7) using only two possible input states on each input (V_{low}= 0 V and V_{high} = 1). Draw a schematic of your circuit and label all the resistors. *HINT: Write down the binary numbers from 000 binary = 0 in decimal to 111 binary = 7 in decimal.* Think about the relative values of R_1, R_2, and R_3. b. Draw a table that lists the input voltages and corresponding output voltages to create $\|V_{out}\|$= 0,1,2,3,4,5,6, and 7.
Question 3	**Integrator** So far you have been looking at the output versus frequency (Bode plots). Now you will also consider the output versus time. a. Design an integrator based on Fig. 3 and the discussion above. Choose values for the components (resistors and capacitors) and describe why you chose those values. b. List some possible applications for the integrator. c. Sketch the time response of your circuit (or you could use Mathematica if you wish) for the following input waveforms: square wave, triangle wave, and sine wave. Hint: you can use your calculus.
Question 4	**Lab activities** a. Read through all of the lab steps and identify the step (or sub-step) that you think will be the most challenging. b. List at least one question you have about the lab activity.

LF356 PIN OUT AND SCHEMATIC

All op-amp circuits start out by making the basic power connections. Op-amps are active components, which means they need external power to function unlike passive components such as resistors.

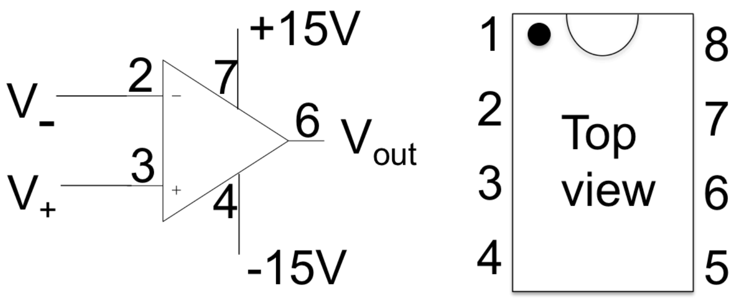

Figure 4. LF356 pin-out and schematic.

GENERAL OP-AMP TIPS (SAME AS LAB 4)

These are reminders of the basics steps you should always follow when working with op-amps.

a. This experiment will use both +15V and -15V to power the LF356 op-amp. Turn off the power while wiring your op-amp. Everyone makes mistakes in wiring-up circuits. Thus, it's a good idea to check your circuit before applying power. Fig. 4 shows a pin-out for the LF356 chip. Familiarize yourself with the layout.

The following procedure will help you wire up a circuit accurately:

1. Draw a complete schematic in your lab book, including all ground and power connections, and all IC pin numbers. Try to layout your prototype so the parts are arranged in the same way as on the schematic, as far as possible.
2. Measure all resistor and capacitor values before putting them in the circuit. Be careful with unit prefixes. It is easy to mistake a 1 nF capacitor for a 1 μF one.
3. Adhere to a color code for wires. For example:

0 V (ground)	Black
+15 V	Red
-15 V	Blue

b. The op-amp chip sits across a groove in the prototyping board. Before inserting a chip, gently straighten the pins. After insertion, check visually that no pin is broken or bent under the chip. To remove the chip, use a small screwdriver in the groove to pry it out.

c. You will have less trouble with spontaneous oscillations if the circuit layout is neat and compact, in particular the feedback path should be as short as possible to reduced unwanted capacitive coupling and lead inductance. Hint: do not wire over the chip but around it.

d. To help prevent spontaneous oscillations due to unintended coupling via the power supplies, use bypass capacitors to filter the supply lines. A bypass capacitor between each power supply lead and ground will provide a miniature current "reservoir" that can quickly supply current when needed. This capacitor is normally in the range 1 μF – 10 μF. The exact value is usually unimportant. Compact capacitors in this range are usually electrolytic, tantalum, or aluminum and are ***polarized***, meaning that one terminal must always be positive relative to the other. The capacitors are labeled which side is which, but each manufacture uses different markings. If you put a polarized capacitor in backwards, it will burn out. Bypass capacitors should be placed as close as possible to the op-amp pins.

e. Figure 4 below shows the basic layout to test your op-amp circuits. The test circuit from the previous lab (Lab 4 Fig. 5) (voltage follower with input grounded) provides a quick way to check if your op-amp is broken.

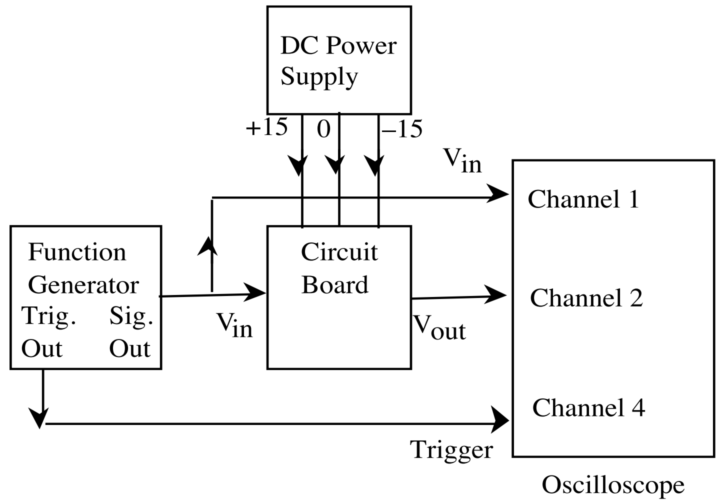

Figure 5. Test and measurement setup for op-amp circuits.

INVERTING AMPLIFIER

Step 1	**Frequency Dependent Gain** a. Build the inverting amplifier shown in Fig. 1, with R_F = 100 kΩ and R = 10 kΩ. Measure R and R_F with the DMM before inserting them into the circuit board. Predict G_0 and f_B from these measured values and the op-amp's value of f_T from the data sheet. (You should be able to review your lab-prep work here too!) b. Use the function generator to measure the low frequency gain. What frequency should you use to test the low frequency gain (i.e., what frequency should the signal be below?) Consider the gain-bandwidth product and how it relates to your circuit. What is the predicted gain for the frequency you chose? Measure the low frequency gain G_0 by measuring V_{in} and V_{out} using the scope. Do your measurements agree with your predictions? c. Predict the 3 dB frequency for your circuit. Include your calculations in your lab book. Now, determine the 3 dB frequency experimentally. Describe the procedure you followed to determine f_B. Does your measurement agree with your prediction? Explicitly record what criteria you used to determine whether or not the model and measurements agree. d. Using the gain-bandwidth relation and your measurements of G_0 and f_B to determine f_T for your op-amp. Does your measured value of f_T agree with the one from the datasheet? e. Measure the frequency dependence of your circuit. Measure the gain at every decade in frequency from 10 MHz down to 10 Hz. Should you use a 10X probe or coax cable to make your measurements? Explain your reasoning. Plot your measurements and predicted gain curve on the same plot. Where, if at all, is the simple model of the op-amp circuit not valid? Suggest possible model refinements and/or physical system refinements to get better agreement between the model predictions and measurements.

SUMMING AMPLIFIER

Step 2	**Summing amplifier – first tests** a. Modify your basic inverting op-amp circuit to make it a summing amplifier as shown in Fig. 2. Use the component values you determined in your lab prep for the resistors. Check with an instructor to see that these values make sense. Draw the schematic in your lab book and label all components. Measure the resistors before inserting them into your circuit and record the values. b. Determine the transfer function for your exact component values. What is V_{out} in terms of V_1, V_2, and V_3? c. Confirm your summing amplifier is working according to your model by measuring V_{out} for any set of V_{in} voltages. Predict the output voltage for your set of test input voltages and measure V_{out}. What is the best available measurement device to make these measurements? Why did you choose that device? Does your measurement agree with your prediction?
Step 3	**Summing amplifier – Digital-to-Analog conversion** a. Use your summing amplifier in "digital to analog conversion mode" to create integer output voltages from 0 to -7 V from two input voltages (0V or 1V). Predict what set of input voltages is required to get each desired output voltage (*HINT: You did this in your Lab prep.)* Do your measurements agree with your predictions for all eight voltages? How accurately were you able to make integer values of voltage? Describe a way to refine the physical system to more accurately create exact integer voltages.

INTEGRATOR

Step 4	**Integrator** By now, you should be somewhat comfortable with experimental design and reporting of outcomes, especially with op-amps and voltage dividers. In this last section, you will get to practice designing and characterizing an integrator. Your starting point should be the circuit you designed in the prelab. Items you likely wish to include: Describe the circuit you are building and testing. Draw the schematic of the circuit with component values labeled. H&H 4.19-4.20 is a useful resource. List your predictions / models. It is fine to start by using ideal models. How do you plan to test it? Be sure to use square, triangle, and sine waves at various frequencies. The results of the tests with various inputs. Do the results match your model? What didn't match…? How would you refine your model or physical system to get better agreement?